Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	424	717/128	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 15:42
S2	496	717/127	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 15:53
S3	424	717/128	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 16:02
S4	752	714/45	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 16:02
S5	529	702/176	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 16:02
S6	196	702/177	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 16:02
S7	234	702/178	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 16:02
S8	398	monitor$3 and (real-time or (real-adj time)) and (variables or data or values) and register and hardware and (cycle or rates or clock or speed) and firmware and (microcode or micro-code or (micro adj code)) and (fetch$3 or pipelin$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 16:18

S9	213	monitor$3 same (real-time or (real-adj time)) and (variables or data or values) and register and hardware and (cycle or rates or clock or speed or chang$3) and firmware and (microcode or micro-code or (micro adj code)) and (fetch$3 or pipelin$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 16:09
S10	328	monitor$3 same (real-time or (real-adj time)) and ((variables or data or values) near5 (cycle or rates or clock or speed or chang$3)) and ((register or hardware) near5 (cycle or rates or clock$3 or speed or chang$3)) and (firmware near5 (cycle or rates or clock or speed or chang$3 or microcode or micro-code or (micro adj code) or fetch$3 or pipelin$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:13
S11	121	monitor$3 same (real-time or (real adj time)) and ((variables or data or values) near5 (cycle or rates or clock or speed or chang$3)) and ((register or hardware) near5 (cycle or rates or clock$3 or speed or chang$3)) and (firmware near5 (cycle or rates or clock or speed or chang$3 or microcode or micro-code or (micro adj code) or fetch$3 or pipelin$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:14
S12	9	(monitor$3 same (real-time or (real adj time)) and ((variables or data or values) near5 (cycle or rates or clock or speed or chang$3)) and ((register or hardware) near5 (cycle or rates or clock$3 or speed or chang$3)) and (firmware near5 (cycle or rates or clock or speed or chang$3 or microcode or micro-code or (micro adj code) or fetch$3 or pipelin$3))) and (717/??? or 714/??? or 702/???)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 17:01
S13	227	monitor$3 and (real-time or (real adj time)) and (variables or data or values) and register and hardware and (cycle or rates or clock or speed) and firmware and (microcode or micro-code or (micro adj code)) and (fetch$3 or pipelin$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:19

S14	0	(monitor$3 and (real-time or (real adj time)) and (variables or data or values) and register and hardware and (cycle or rates or clock or speed) and firmware and (microcode or micro-code or (micro adj code)) and (fetch$3 or pipelin$3)) and (717/12? or 717/13?)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:19
S15	13	(monitor$3 and (real-time or (real adj time)) and (variables or data or values) and register and hardware and (cycle or rates or clock or speed) and firmware and (microcode or micro-code or (micro adj code)) and (fetch$3 or pipelin$3)) and (717/12? or 717/13? or 714/??? or 702/17?)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:47
S16	7	"6085336".URPN.	USPAT	OR	OFF	2004/10/07 16:42
S17	74768	monitor$3 and (data or variable or value) near5 ((chang$3 or modification) near3 rate or metric or frequenc$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:50
S18	5153	monitor$3 and (register or hardware) near5 ((chang$3 or modification) near3 rate or metric or frequenc$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:50
S19	629	(monitor$3 same (data or variable or value)) near5 ((chang$3 or modification) near3 (rate or metric or frequenc$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 17:01
S20	16	(monitor$3 same (hardware or register)) near5 ((chang$3 or modification) near3 (rate or metric or frequenc$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:53
S21	0	(monitor$3 same (firmware or fpga or eeprom or reconfigurable)) near5 ((chang$3 or modification) near3 (rate or metric or frequenc$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:54
S22	37	(monitor$3 same (firmware or fpga or eeprom or reconfigurable)) same ((chang$3 or modification) near3 (rate or metric or frequenc$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:55

S23	2	((monitor$3 same (data or variable or value)) near5 ((chang$3 or modification) near3 (rate or metric or frequenc$3))) and ((monitor$3 same (hardware or register)) near5 ((chang$3 or modification) near3 (rate or metric or frequenc$3)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 16:59
S24	240	monitor$3 near8 (software and (hardware or register) and (firmware or fpga or eeprom or eprom or reconfigurable))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 17:00
S25	0	((monitor$3 same (data or variable or value)) near5 ((chang$3 or modification) near3 (rate or metric or frequenc$3))) and (monitor$3 near8 (software and (hardware or register) and (firmware or fpga or eeprom or eprom or reconfigurable)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 17:01
S26	25	(monitor$3 near8 (software and (hardware or register) and (firmware or fpga or eeprom or eprom or reconfigurable))) and (717/??? or 714/??? or 702/???)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 18:08
S27	0	"6754854".URPN.	USPAT	OR	OFF	2004/10/07 17:19
S28	6	("5581482" | "5828851" | "5995916" | "6351724" | "6453268" | "6564170").PN.	USPAT	OR	OFF	2004/10/07 17:19
S29	51	"5768500".URPN.	USPAT	OR	OFF	2004/10/07 17:40
S30	51	"5768500".URPN.	USPAT	OR	OFF	2004/10/07 17:44
S31	154838	(monitor$3 or sampl$3 or profil$3 or collect$3) near3 (rates or cycles or clocks)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 18:09
S32	3	(monitor$3 or sampl$3 or profil$3 or collect$3) adj (fetch$3 adj (rates or cycles or clocks))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 18:10
S33	110	(monitor$3 or sampl$3 or profil$3 or collect$3) adj (change adj (rates or clocks))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 18:10

S34	98	(monitor$3 or sampl$3 or profil$3 or collect$3) same (value or data or variable) adj (change adj (rates or clocks))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 18:11
S35	0	((monitor$3 or sampl$3 or profil$3 or collect$3) adj (cycle adj (rates or clocks))) and ((monitor$3 or sampl$3 or profil$3 or collect$3) same (value or data or variable) adj (change adj (rates or clocks)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 18:11
S36	45	(monitor$3 or sampl$3 or profil$3 or collect$3) adj (cycle adj (rates or clocks))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/07 18:11
S37	6	("4901259" "5377122" "6230295").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 08:11
S39	272	(monitor$3 or sampl$3 or trac$3) and (real-time or (real adj time)) and ((variable or data or value) and register and hardware and (cycle or rate or change or clock or speed or frequency or duration or measurement) and firmware and (microcode or micro-code or (micro adj code)) and (fetch$3 or pipelin$3) and (log$4 or verification or self-test or diagnostic or trigger or start/stop or breakpoint or timestamp or time-stamp or non-intrusive or comparator or difference or bit or (time adj stamp)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:25
S40	4	("4901259" "5377122").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 06:57
S41	16	("6708303" "6754854" "6453268" "5995916" "5581482" "6754890"). pn. or "20020002443"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:23
S42	0	S39 and S41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:24

S43	0	S39 and S40	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:24
S44	132	(monitor$3 or sampl$3 or trac$3)same (real-time or (real adj time)) and ((variable or data or value) and register and hardware and (cycle or rate or change or clock or speed or frequency or duration or measurement) and firmware and (microcode or micro-code or (micro adj code)) and (fetch$3 or pipelin$3) and (log$4 or verification or self-test or diagnostic or trigger or start/stop or breakpoint or timestamp or time-stamp or non-intrusive or comparator or difference or bit or (time adj stamp)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:26
S45	27	(monitor$3 or sampl$3 or trac$3)same (real-time or (real adj time) same (cycle or rate or change or clock or speed or frequency or duration or measurement)) and ((variable or data or value) and register and hardware and firmware and (microcode or micro-code or (micro adj code)) and (fetch$3 or pipelin$3) and (verification or self-test or diagnostic or trigger or start/stop or breakpoint or non-intrusive or comparator or difference or bit)) and ((time adj stamp) or timestamp or time-stamp) and log$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:36
S46	15413	(monitor$3 or sampl$3 or trac$3)same (real-time or (real adj time)) and ((whole or total or complete or entire) near3 (system or monitor$3 or sampl$3 or trac$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:37
S47	16053	(monitor$3 or sampl$3 or trac$3)same (real-time or (real adj time)) and ((whole or total or complete or entire) near3 (system or monitor$3 or sampl$3 or trac$3 or apparatus))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:38

S48	1650	(monitor$3 or sampl$3 or trac$3)same (real-time or (real adj time))same ((whole or total or complete or entire) near3 (system or monitor$3 or sampl$3 or trac$3 or apparatus))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:50
S49	650	(monitor$3 or sampl$3 or trac$3)same (real-time or (real adj time))same ((whole or total or complete or entire) near3 (system or apparatus))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:39
S50	650	(monitor$3 or sampl$3 or trac$3) same (real-time or (real adj time)) same ((whole or total or complete or entire) near3 (system or apparatus))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:47
S51	272	(monitor$3 or sampl$3 or trac$3) near5 (real-time or (real adj time)) same ((whole or total or complete or entire) near3 (system or apparatus))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:46
S52	74	(monitor$3 or sampl$3 or trac$3) near5 (real-time or (real adj time)) near5 ((whole or total or complete or entire) near3 (system or apparatus))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:41
S53	6	S39 and S49	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:41
S54	0	(monitor$3 or sampl$3 or trac$3) near5 (real-time or (real adj time)) same ((whole or total or complete or entire) near3 (system or apparatus)) and 717/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:46
S55	2	(monitor$3 or sampl$3 or trac$3) same (real-time or (real adj time)) same ((whole or total or complete or entire) near3 (system or apparatus)) and 717/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:47
S56	6	(monitor$3 or sampl$3 or trac$3)same (real-time or (real adj time))same ((whole or total or complete or entire) near3 (system or monitor$3 or sampl$3 or trac$3 or apparatus)) and 717/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:54
S57	11597	(monitor$3 or sampl$3 or trac$3) near3 (system or apparatus) same (real-time or (real adj time))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:55

S58	3508	S47 and S57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:56
S59	1650	S47 and S48	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:56
S60	650	S47 and S49	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 07:56
S61	48	S47 and S57 and 717/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 10:29
S62	6	S47 and S48 and 717/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 10:29
S63	2	S47 and S49 and 717/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 10:29
S64	52	S61 or S62 or S63	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 08:34
S65	0	("6748558").URPN.	USPAT	OR	OFF	2004/12/21 08:29
S66	27	("4023142" | "4268902" | "4277827" | "4312066" | "4314333" | "4441154" | "4513418" | "4519078" | "4594711" | "4597080" | "4601034" | "4615029" | "4621363" | "4680733" | "4687988" | "4698588" | "4701921" | "4710931" | "4710933" | "4788683" | "4801870" | "4855954" | "4857835" | "4872169" | "4879688" | "4896296" | "5103450").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/21 08:29

S67	17	(software and hardware and firmware) near2 register and (monitor$3 or trac$3 or profil$3 or sampl$3 or instrument$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 08:35
S68	222	S47 and S57 and 702/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 10:29
S69	83	S47 and S48 and 702/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 10:29
S70	41	S47 and S49 and 702/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 10:29
S71	30	S68 and S69 and S70	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 10:30
S72	13	("5964837" | "5983278" | "5999908" | "6023507" | "6078956" | "6285658" | "6363421" | "6427063" | "6469991" | "6601020" | "6633835" | "6701363" | "6714976").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/21 16:29
S73	18	noninvasively and (international and business).as.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/21 16:36
S74	10	("5875294" "5752062" "5797019" "5751945" "5729726" "5748855" "5691920" "5835702" "5881306" "5802273").pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/21 16:37
S75	4	("4901259" "5377122").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/22 07:04
S76	1	"full system monitor"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 09:43

S77	15514	"system monitor"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 10:00
S78	37	"system monitor" and ((data or variable or value) near3 "change rate")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 09:45
S79	0	"system monitor" and ((hardware or register) near3 "change rate")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 09:45
S80	0	"system monitor" and ((hardware or register) near3 "cycle rate")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 09:46
S81	412	"system monitor" and ((hardware or register) near3 ("cycle rate" or clock))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 09:47
S83	46	"system monitor" and (fetch$3 near3(rate or clock))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:01
S84	36	"system monitor" and (fetch$3 near3(rate or clock)) and (firmware or rom or bios or kernel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:14
S85	0	S78 and S81	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 09:59
S86	0	S78 and S84	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:00
S87	0	S81 and S84	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:00

S88	471	"system monitor" same ("real time" or "on the fly")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 10:00
S89	5	S88 and S81	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:01
S90	0	("system monitor" or "system trace" or "performance analysis" or "system analysis") and "register level trace" and (frequency or timing or clock$3 or rate) and (trigger$3 or interrupt$3) and ("real time" or "realtime" or "real-time" or "on the fly")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:20
S91	0	("system monitor" or "system trace" or "performance analysis" or "system analysis") and "register level trace" and (frequency or timing or clock$3 or rate) and (trigger$3 or interrupt$3) and ("real time" or "realtime" or "real-time" or "on the fly" or "while executing" or "while running")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:21
S92	0	("monitor" or "trace" or "performance analysis" or "system analysis") and "register level trace" and (frequency or timing or clock$3 or rate) and (trigger$3 or interrupt$3) and ("real time" or "realtime" or "real-time" or "on the fly" or "while executing" or "while running")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:21
S93	18178	("monitor" or "trace" or "performance analysis" or "system analysis") and register and (frequency or timing or clock$3 or rate) and (trigger$3 or interrupt$3) and ("real time" or "realtime" or "real-time" or "on the fly" or "while executing" or "while running")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:21

S94	466	("monitor" or "trace" or "performance analysis" or "system analysis") same register same (frequency or timing or clock$3 or rate) same (trigger$3 or interrupt$3) and ("real time" or "realtime" or "real-time" or "on the fly" or "while executing" or "while running")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:29
S95	143	("monitor" or "trace" or "performance analysis" or "system analysis") same register same (frequency or timing or clock$3 or rate) same (trigger$3 or interrupt$3) and ("real time" or "realtime" or "real-time" or "on the fly" or "while executing" or "while running") and (firmware or bios or rom or microcode or micro-code or "micro code") and fetch$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:24
S96	0	"word fetch rates"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:24
S97	259	"word fetch"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:25
S98	4	"fetch rates" same (monitor$3 or trac$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:24
S99	8	"word fetch" same (monitor$3 or trac$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:25
S100	248	(comparator or boolean or (test$3 near2 equal$3)) and ("monitor" or "trace" or "performance analysis" or "system analysis") same register same (frequency or timing or clock$3 or rate) same (trigger$3 or interrupt$3) and ("real time" or "realtime" or "real-time" or "on the fly" or "while executing" or "while running")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 10:31

S101	240	(comparator or (test$3 near2 equal$3)) and ("monitor" or "trace" or "performance analysis" or "system analysis") same register same (frequency or timing or clock$3 or rate) same (trigger$3 or interrupt$3) and ("real time" or "realtime" or "real-time" or "on the fly" or "while executing" or "while running")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 11:14
S102	2	"5970439".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 11:14
S103	10	("5970439").URPN.	USPAT	OR	OFF	2005/06/07 11:15
S104	5	("6338159").URPN.	USPAT	OR	OFF	2005/06/07 11:23
S105	10	("6351724").URPN.	USPAT	OR	OFF	2005/06/07 11:33
S106	1	"5377122".pn.	USPAT	OR	OFF	2005/06/07 11:33
S107	25	("5377122").URPN.	USPAT	OR	OFF	2005/06/07 12:53
S108	1	"5970436".pn. and (compar$4 or equal$3)	USPAT	OR	OFF	2005/06/07 12:54
S109	0	"5970439".pn. and (compar$4 or equal$3)	USPAT	OR	OFF	2005/06/07 12:54
S110	1	"5970439".pn. and (compar$4 or equal$3 or condition$2)	USPAT	OR	OFF	2005/06/07 12:59
S111	1	"5970439".pn. and poll$3	USPAT	OR	OFF	2005/06/07 13:01
S112	1	"5970439".pn. and (read$3 or writ$3 or read/write)	USPAT	OR	OFF	2005/06/07 13:01
S113	1044	714/704.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/07 17:04
S114	214	714/704.ccls. and (monitor$3 or trac$3) and frequency	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/07 17:04

S115	20	714/704.ccls. and (monitor$3 or trac$3) and frequency same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:05
S116	6	714/708.ccls. and (monitor$3 or trac$3) and frequency same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:05
S117	21	714/712.ccls. and (monitor$3 or trac$3) and frequency same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:05
S118	11	714/814.ccls. and (monitor$3 or trac$3) and frequency same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:05
S119	25	714/38.ccls. and (monitor$3 or trac$3) and frequency same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:05
S120	14	714/45.ccls. and (monitor$3 or trac$3) and frequency same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:08
S121	86	frequency near3 (use near3 bit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:06
S122	0	S121 and S115	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:07
S123	0	S121 and S116	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:07
S124	0	S121 and S117	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:07

S125	0	S121 and S118	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:07
S126	0	S121 and S119	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:07
S127	0	S121 and S120	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:07
S128	442	717/127.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:07
S129	10	717/127.ccls. and (monitor$3 or trac$3) and frequency same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:08
S130	7	717/127.ccls. and (monitor$3 or trac$3) and frequency same register and bit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:10
S131	0	717/127.ccls. and (monitor$3 or trac$3) and frequency same register and bit and hardware and firmware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 17:10

Published since January 1980 and Published before February 2002 Found **1,284** of **105**

Terms used **monitor frequency bit use**

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1 A firmware monitor to support vertical migration decisions in the UNIX operating system
B. Holtkamp, H. Kaestner
October 1982 **ACM SIGMICRO Newsletter , Proceedings of the 15th annual workshop on Microprogramming MICRO 15**, Volume 13 Issue 4
Publisher: IEEE Press, ACM Press
Full text available: pdf(686.64 KB) Additional Information: full citation, abstract, references, citings, index terms

From a methodological point of view vertical migration involves the following four steps: identification of migration objects, prediction of expected system improvements, implementation, and verification of the results. In this paper a firmware monitor is presented as a support tool for the first and fourth step. The application environment for this monitor is a PDP-11/60 with writable control store running the UNIX operating system. Based upon a UNIX system model the requirement ...

2 Studying long-term system use
Judy Kay, Richard C. Thomas
July 1995 **Communications of the ACM**, Volume 38 Issue 7
Publisher: ACM Press
Full text available: pdf(389.77 KB) Additional Information: full citation, references, citings, index terms

3 Use of a soundcard in teaching audio frequency and analog modem communications
Martin H. Levin
June 1999 **ACM SIGCSE Bulletin , Proceedings of the 4th annual SIGCSE/SIGCUE ITiCSE conference on Innovation and technology in computer science education ITiCSE '99**, Volume 31 Issue 3
Publisher: ACM Press
Full text available: pdf(636.55 KB) Additional Information: full citation, abstract, references, index terms

Sound cards have become standard features of personal computers in the home, office and classroom. This paper demonstrates the usefulness of these inexpensive devices in the teaching of some of the basic and not so basic concepts of communications. These devices can be very effective in the explanation of amplitude, frequency and frequency multiplexed circuits, as well as modem handshake standards.

Keywords: analog telephone systems, dual tone multiple frequencies, handshakes, soundcards, telephone modems, visual pictorization

4 A trace-based evaluation of adaptive error correction for a wireless local area network
David A. Eckhardt, Peter Steenkiste
December 1999 **Mobile Networks and Applications**, Volume 4 Issue 4
Publisher: Kluwer Academic Publishers
Full text available: pdf(243.29 KB) Additional Information: full citation, abstract, references, citings, index terms

Wireless transmissions are highly susceptible to noise and interference. As a result, the error characteristics of a wireless link may vary widely depending on environmental factors such as location of the communicating systems and activity of competing radiation sources, making error control a difficult task. In this paper we evaluate error control strategies for a wireless LAN. Based on low-level packet traces of WaveLAN, we first show that forward error correction (FEC) is effective in r ...

5 A framework for reducing the cost of instrumented code
Matthew Arnold, Barbara G. Ryder
May 2001 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2001 conference on Programming language design and implementation PLDI '01**, Volume 36 Issue 5
Publisher: ACM Press
Full text available: pdf(1.78 MB) Additional Information: full citation, abstract, references, citings, index terms

Instrumenting code to collect profiling information can cause substantial execution overhead. This overhead makes instrumentation difficult to perform at runtime, often preventing many known *offline* feedback-directed optimizations from being used in online systems. This paper presents a general framework for performing *instrumentation sampling* to reduce the overhead of previously expensive instrumentation. The framework is simple and effective, using code-duplication and *coun* ...

6 Physical layer driven protocol and algorithm design for energy-efficient wireless sensor networks
Eugene Shih, Seong-Hwan Cho, Nathan Ickes, Rex Min, Amit Sinha, Alice Wang, Anantha Chandrakasan
July 2001 **Proceedings of the 7th annual international conference on Mobile computing and networking**
Publisher: ACM Press
Full text available: pdf(782.22 KB) Additional Information: full citation, abstract, references, citings, index terms

The potential for collaborative, robust networks of microsensors has attracted a great deal of research attention. For the most part, this is due to the compelling applications that will be enabled once wireless microsensor networks are in place; location-sensing, environmental

sensing, medical monitoring and similar applications are all gaining interest. However, wireless microsensor networks pose numerous design challenges. For applications requiring long-term, robust sensing, such as milit ...

7 A proposal for an architectural approach which apparently solves all known software-based internal computer security problems
Richard LeRoy Routh
July 1984 **ACM SIGOPS Operating Systems Review**, Volume 18 Issue 3
Publisher: ACM Press
Full text available: pdf(468.83 KB) Additional Information: full citation, abstract, references, citings

Computer security violations have become a serious threat to both corporate and national defense activities. By extending the work done by Myers et. el. in the development of the capability based addressing system for the SWARD at IBM, a Computer architectural approach has been developed which solves all known software based internal computer security problems. The problems solved include (but are not restricted to):(1) Violations of priority and access priveleges by highly skilled users,(2) The ...

8 On the cost of monitoring and reorganization of object bases for clustering
Carsten A. Gerlhof, Alfons Kemper, Guido Moerkotte
September 1996 **ACM SIGMOD Record**, Volume 25 Issue 3
Publisher: ACM Press
Full text available: pdf(606.93 KB) Additional Information: full citation, abstract, citings, index terms

Clustering is one of the most effective means to enhance the performance of object base applications. Consequently, many proposals exist for algorithms computing good object placements depending on the application profile. However, in an effective object base reorganization tool the clustering algorithm is only one constituent. In this paper, we report on our object base reorganization tool that covers all stages of reorganizing the objects: the application profile is determined by a monito ...

9 Measurement and analysis of instruction use in the VAX-11/780
Douglas W. Clark, Henry M. Levy
April 1982 **Proceedings of the 9th annual symposium on Computer Architecture**
Publisher: IEEE Computer Society Press
Full text available: pdf(723.49 KB) Additional Information: full citation, abstract, references, citings, index terms

This paper reports measurements of instruction set use on the VAX-11/780 computer. A hardware monitor was used to measure the frequency and time taken by each VAX instruction. Data from benchmark programs, a compiler, a linker, and a synthetic timesharing workload are reported. Results show that although some programs rely on a small set of instructions, different applications use the instruction set in different ways.

10 The design and development of a dynamic program behavior measurement tool for the Intel 8086/88
R. J. Schwartz
June 1989 **ACM SIGARCH Computer Architecture News**, Volume 17 Issue 4
Publisher: ACM Press

Full text available: pdf(906.01 KB) Additional Information: full citation, abstract, index terms

When modeling a computer system, it is necessary to study the system's dynamic behavior. Examples of this behavior are branching frequency and operating system usage patterns. The dynamic properties of the system characterize its performance. Models make assumptions about such behavior, but require genuine data to validate the assumptions. In this article, we present a measurement tool that will collect and analyze dynamic program information for the Intel 8086/88. The design and development of ...

11 The SHRIMP performance monitor: design and applications

Margaret Martonosi, Douglas W. Clark, Malena Mesarina

January 1996 **Proceedings of the SIGMETRICS symposium on Parallel and distributed tools**
Publisher: ACM Press

Full text available: pdf(1.01 MB) Additional Information: full citation, references, citings, index terms

12 The mimola design system: Tools for the design of digital processors

Peter Marwedel

June 1984 **Proceedings of the 21st conference on Design automation**
Publisher: IEEE Press

Full text available: pdf(642.29 KB) Additional Information: full citation, abstract, references, citings, index terms

The MIMOLA design method is a method for the design of digital processors from a very high-level bevavioral specification. A key feature of this method is the synthesis of a processor from a description of programs which are expected to be typical for the applications of that processor. Design cycles, in which the designer tries to improve automatically generated hardware structures, are supported by a retargetable microcode generator and by an utilization and performance analyzer. This pap ...

13 Residual test coverage monitoring

Christina Pavlopoulou, Michal Young

May 1999 **Proceedings of the 21st international conference on Software engineering**
Publisher: IEEE Computer Society Press

Full text available: pdf(979.59 KB) Additional Information: full citation, references, citings, index terms

Keywords: coverage, instrumentation, testing

14 An analysis of a mesa instruction set using dynamic instruction frequencies

Gene McDaniel

March 1982 **ACM SIGARCH Computer Architecture News , ACM SIGPLAN Notices , Proceedings of the first international symposium on Architectural support for programming languages and operating systems ASPLOS-I**, Volume 10 , 17 Issue 2 , 4
Publisher: ACM Press

Full text available: pdf(948.65 KB) Additional Information: full citation, abstract, references, citings, index terms

The Mesa architecture is implemented on a variety of processors, and dynamic instruction frequency data for two programs is used to analyze the architecture in an implementation independent fashion. The Mesa compiler allocates variables in an order based upon their static frequency of use, and measurements are provided that show that these static predictions predict run time usage as well. We provide an evaluation of the advantages and costs of Mesa's compact byte encoding, its r ...

15 Measuring VAX 8800 performance with a histogram hardware monitor

D. W. Clark, P. J. Bannon, J. B. Keller

May 1988 **ACM SIGARCH Computer Architecture News , Proceedings of the 15th Annual International Symposium on Computer architecture ISCA '88**, Volume 16 Issue 2

Publisher: IEEE Computer Society Press, ACM Press

Full text available: pdf(1.04 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper reports the results of a study of VAX 8800 processor performance using a hardware monitor that collects histograms of the processor's micro-PC and memory bus status. The monitor keeps a count of all machine cycles executed at each micro-PC location, as well as counting all occurrences of each bus transaction. It can measure a running system without interfering with it, and this paper's results are based on measurements of live timesharing. Because the 8800 is a microcoded machine ...

16 Arithmetic coding revisited

Alistair Moffat, Radford M. Neal, Ian H. Witten

July 1998 **ACM Transactions on Information Systems (TOIS)**, Volume 16 Issue 3

Publisher: ACM Press

Full text available: pdf(487.26 KB) Additional Information: full citation, abstract, references, citings, index terms

Over the last decade, arithmetic coding has emerged as an important compression tool. It is now the method of choice for adaptive coding on myltisymbol alphabets because of its speed, low storage requirements, and effectiveness of compression. This article describes a new implementation of arithmetic coding that incorporates several improvements over a widely used earlier version by Witten, Neal, and Cleary, which has become a de facto standard. These improvements include f ...

Keywords: approximate coding, arithmetic coding, text compression, word-based model

17 The design and implementation of tripwire: a file system integrity checker

Gene H. Kim, Eugene H. Spafford

November 1994 **Proceedings of the 2nd ACM Conference on Computer and communications security**

Publisher: ACM Press

Full text available: pdf(1.22 MB) Additional Information: full citation, abstract, references, citings, index terms

At the heart of most computer systems is a file system. The file system contains user data, executable programs, configuration and authorization information, and (usually) the base

executable version of the operating system itself. The ability to monitor file systems for unauthorized or unexpected changes gives system administrators valuable data for protecting and maintaining their systems. However, in environments of many networked heterogeneous platforms with different policies and softw ...

18 Implementing the IEEE 802.5 token-ring standard
M. C. Hamner, J. T. Carlo
November 1986 **Proceedings of 1986 ACM Fall joint computer conference**
Publisher: IEEE Computer Society Press
Full text available: pdf(579.40 KB) Additional Information: full citation, references, citings, index terms

19 Reconfigurable scan chains: a novel approach to reduce test application time
Sridhar Narayanan, Melvin A. Breuer
November 1993 **Proceedings of the 1993 IEEE/ACM international conference on Computer-aided design**
Publisher: IEEE Computer Society Press
Full text available: pdf(762.86 KB) Additional Information: full citation, references, citings

20 Local networks
William Stallings
March 1984 **ACM Computing Surveys (CSUR)**, Volume 16 Issue 1
Publisher: ACM Press
Full text available: pdf(3.01 MB) Additional Information: full citation, abstract, references, citings, index terms, review

The rapidly evolving field of local network technology has produced a steady stream of local network products in recent years. The IEEE 802 standards that are now taking shape, because of their complexity, do little to narrow the range of alternative technical approaches and at the same time encourage more vendors into the field. The purpose of this paper is to present a systematic, organized overview of the alternative architectures for and design approaches to local networks.

...

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Terms used
monitor frequency bit use hardware firmware real time

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1 A firmware monitor to support vertical migration decisions in the UNIX operating system

B. Holtkamp, H. Kaestner

October 1982 **ACM SIGMICRO Newsletter , Proceedings of the 15th annual workshop on Microprogramming MICRO 15**, Volume 13 Issue 4

Publisher: IEEE Press, ACM Press

Full text available: pdf(686.64 KB) Additional Information: full citation, abstract, references, citings, index terms

From a methodological point of view vertical migration involves the following four steps: identification of migration objects, prediction of expected system improvements, implementation, and verification of the results. In this paper a firmware monitor is presented as a support tool for the first and fourth step. The application environment for this monitor is a PDP-11/60 with writable control store running the UNIX operating system. Based upon a UNIX system model the requirement ...

2 Systems 1: A wireless sensor network For structural monitoring

Ning Xu, Sumit Rangwala, Krishna Kant Chintalapudi, Deepak Ganesan, Alan Broad, Ramesh Govindan, Deborah Estrin

November 2004 **Proceedings of the 2nd international conference on Embedded networked sensor systems**

Publisher: ACM Press

Full text available: pdf(731.28 KB) Additional Information: full citation, abstract, references, index terms

Structural monitoring---the collection and analysis of structural response to ambient or forced excitation--is an important application of networked embedded sensing with significant commercial potential. The first generation of sensor networks for structural monitoring are likely to be data acquisition systems that collect data at a single node for centralized processing. In this paper, we discuss the design and evaluation of a wireless sensor network system (called Wisden for structural dat ...

Keywords: Wisden, sensor network, structural health monitoring

3 System-level power optimization: techniques and tools
Luca Benini, Giovanni de Micheli
April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 2
Publisher: ACM Press
Full text available: pdf(385.22 KB) Additional Information: full citation, abstract, references, citings, index terms

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic sytems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, communication, and storage units, and we review methods of reducing their energy consumption. We also study models for analyzing the energy cost of software, and methods for energy-efficient software design and compilation. This survery ...

4 Virtual machine monitors: Terra: a virtual machine-based platform for trusted computing
Tal Garfinkel, Ben Pfaff, Jim Chow, Mendel Rosenblum, Dan Boneh
October 2003 **Proceedings of the nineteenth ACM symposium on Operating systems principles**
Publisher: ACM Press
Full text available: pdf(140.31 KB) Additional Information: full citation, abstract, references, citings, index terms

We present a flexible architecture for trusted computing, called Terra, that allows applications with a wide range of security requirements to run simultaneously on commodity hardware. Applications on Terra enjoy the semantics of running on a separate, dedicated, tamper-resistant hardware platform, while retaining the ability to run side-by-side with normal applications on a general-purpose computing platform. Terra achieves this synthesis by use of a *trusted virtual machine monitor* (TVMM ...

Keywords: VMM, attestation, authentication, trusted computing, virtual machine, virtual machine monitor

5 Real-time avionics in Ada 83
Don K. Silvasi-Patchin
November 1995 **Proceedings of the conference on TRI-Ada '95: Ada's role in global markets: solutions for a changing complex world**
Publisher: ACM Press
Full text available: pdf(1.01 MB) Additional Information: full citation, references

6 Pen computing: a technology overview and a vision
André Meyer
July 1995 **ACM SIGCHI Bulletin**, Volume 27 Issue 3
Publisher: ACM Press
Full text available: pdf(5.14 MB) Additional Information: full citation, abstract, citings, index terms

This work gives an overview of a new technology that is attracting growing interest in public as well as in the computer industry itself. The visible difference from other technologies is in the use of a pen or pencil as the primary means of interaction between a user and a machine, picking up the familiar pen and paper interface metaphor. From this follows a set of consequences that will be analyzed and put into context with other emerging technologies and visions.Starting with a short historic ...

7 Deployment experience: Design and deployment of industrial sensor networks: experiences from a semiconductor plant and the north sea

Lakshman Krishnamurthy, Robert Adler, Phil Buonadonna, Jasmeet Chhabra, Mick Flanigan, Nandakishore Kushalnagar, Lama Nachman, Mark Yarvis

November 2005 **Proceedings of the 3rd international conference on Embedded networked sensor systems SenSys '05**

Publisher: ACM Press

Full text available: pdf(677.48 KB) Additional Information: full citation, abstract, references, index terms

Sensing technology is a cornerstone for many industrial applications. Manufacturing plants and engineering facilities, such as shipboard engine rooms, require sensors to ensure product quality and efficient and safe operation. We focus on one representative application, preventative equipment maintenance, in which vibration signatures are gathered to predict equipment failure. Based on application requirements and site surveys, we develop a general architecture for this class of industrial appli ...

Keywords: embedded hardware design, industrial applications of sensor networks

8 Wide-area monitoring of mobile objects: Implementing software on resource-constrained mobile sensors: experiences with Impala and ZebraNet

Ting Liu, Christopher M. Sadler, Pei Zhang, Margaret Martonosi

June 2004 **Proceedings of the 2nd international conference on Mobile systems, applications, and services MobiSys '04**

Publisher: ACM Press

Full text available: pdf(3.14 MB) Additional Information: full citation, abstract, references, citings, index terms

ZebraNet is a mobile, wireless sensor network in which nodes move throughout an environment working to gather and process information about their surroundings[10]. As in many sensor or wireless systems, nodes have critical resource constraints such as processing speed, memory size, and energy supply; they also face special hardware issues such as sensing device sample time, data storage/access restrictions, and wireless transceiver capabilities. This paper discusses and evaluates ZebraNet's syst ...

Keywords: event handling, middleware system, network communications, operation scheduling, sensor networks

9 MemorIES3: a programmable, real-time hardware emulation tool for multiprocessor server design

Ashwini Nanda, Kwok-Ken Mak, Krishnan Sugarvanam, Ramendra K. Sahoo, Vijayaraghavan Soundararajan, T. Basil Smith

November 2000 **ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer**

Architecture News , Proceedings of the ninth international conference on Architectural support for programming languages and operating systems ASPLOS-IX, Volume 34 , 28 Issue 5 , 5

Publisher: ACM Press

Full text available: pdf(724.53 KB) Additional Information: full citation, abstract, references, citings, index terms

Modern system design often requires multiple levels of simulation for design validation and performance debugging. However, while machines have gotten faster, and simulators have become more detailed, simulation speeds have not tracked machine speeds, As a result, it is difficult to simulate realistic problem sizes and hardware configurations for a target machine. Instead, researchers have focussed on developing sealing methodologies and running smaller problem sizes and configurations that atte ...

10 MemorIES: a programmable, real-time hardware emulation tool for multiprocessor server design

Ashwini Nanda, Kwok-Ken Mak, Krishnan Sugavanam, Ramendra K. Sahoo, Vijayaraghavan Soundararajan, T. Basil Smith

November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

Publisher: ACM Press

Full text available: pdf(1.84 MB) Additional Information: full citation, abstract, references, index terms

Modern system design often requires multiple levels of simulation for design validation and performance debugging. However, while machines have gotten faster, and simulators have become more detailed, simulation speeds have not tracked machine speeds. As a result, it is difficult to simulate realistic problem sizes and hardware configurations for a target machine. Instead, researchers have focussed on developing scaling methodologies and running smaller problem sizes and configurations that atte ...

11 Software safety: why, what, and how

Nancy G. Leveson

June 1986 **ACM Computing Surveys (CSUR)**, Volume 18 Issue 2

Publisher: ACM Press

Full text available: pdf(4.18 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Software safety issues become important when computers are used to control real-time, safety-critical processes. This survey attempts to explain why there is a problem, what the problem is, and what is known about how to solve it. Since this is a relatively new software research area, emphasis is placed on delineating the outstanding issues and research topics.

12 Implementation aspects of a SPARC V9 complete machine simulator

Bill Clarke, Adam Czezowski, Peter Strazdins

January 2002 **Australian Computer Science Communications , Proceedings of the twenty-fifth Australasian conference on Computer science - Volume 4 CRPITS '02**, Volume 24 Issue 1

Publisher: Australian Computer Society, Inc. , IEEE Computer Society Press

Full text available: pdf(1.33 MB) Additional Information: full citation, abstract, references, index terms

In this paper we present work in progress in the development of a complete machine simulator for the UltraSPARC, an implementation of the SPARC V9 architecture. The complexity of the

UltraSPARC ISA presents many challenges in developing a reliable and yet reasonably efficient implementation of such a simulator. Our implementation includes a heavily object-oriented design for the simulator modules and infrastructure, caching of repeated computations for performance, adding an OS (system call) emu ...

Keywords: SMP, SPARC V9 ISA, UltraSPARC, complete machine simulator, execution-driven simulation, object-oriented design

13 Survey of code-size reduction methods

Árpád Beszédes, Rudolf Ferenc, Tibor Gyimóthy, André Dolenc, Konsta Karsisto
September 2003 **ACM Computing Surveys (CSUR)**, Volume 35 Issue 3
Publisher: ACM Press
Full text available: pdf(443.89 KB) Additional Information: full citation, abstract, references, index terms

Program code compression is an emerging research activity that is having an impact in several production areas such as networking and embedded systems. This is because the reduced-sized code can have a positive impact on network traffic and embedded system costs such as memory requirements and power consumption. Although code-size reduction is a relatively new research area, numerous publications already exist on it. The methods published usually have different motivations and a variety of appli ...

Keywords: code compaction, code compression, method assessment, method evaluation

14 Physical interface: Fine-grained network time synchronization using reference broadcasts

Jeremy Elson, Lewis Girod, Deborah Estrin
December 2002 **ACM SIGOPS Operating Systems Review**, Volume 36 Issue SI
Publisher: ACM Press
Full text available: pdf(2.10 MB) Additional Information: full citation, abstract, references, citings

Recent advances in miniaturization and low-cost, low-power design have led to active research in large-scale networks of small, wireless, low-power sensors and actuators. Time synchronization is critical in sensor networks for diverse purposes including sensor data fusion, coordinated actuation, and power-efficient duty cycling. Though the clock accuracy and precision requirements are often stricter than in traditional distributed systems, strict energy constraints limit the resources available ...

15 Balancing performance and flexibility with hardware support for network architectures

Ilija Hadžić, Jonathan M. Smith
November 2003 **ACM Transactions on Computer Systems (TOCS)**, Volume 21 Issue 4
Publisher: ACM Press
Full text available: pdf(719.03 KB) Additional Information: full citation, abstract, references, index terms

The goals of performance and flexibility are often at odds in the design of network systems. The tension is common enough to justify an architectural solution, rather than a set of context-specific solutions. The Programmable Protocol Processing Pipeline (P4) design uses programmable hardware to selectively accelerate protocol processing functions. A set of field-

programmable gate arrays (FPGAs) and an associated library of network processing modules implemented in hardware are augmented with so ...

Keywords: FPGA, P4, computer networking, flexibility, hardware, performance, programmable logic devices, programmable networks, protocol processing

16 A relational approach to monitoring complex systems
Richard Snodgrass
May 1988 **ACM Transactions on Computer Systems (TOCS)**, Volume 6 Issue 2
Publisher: ACM Press
Full text available: pdf(3.42 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Monitoring is an essential part of many program development tools, and plays a central role in debugging, optimization, status reporting, and reconfiguration. Traditional monitoring techniques are inadequate when monitoring complex systems such as multiprocessors or distributed systems. A new approach is described in which a historical database forms the conceptual basis for the information processed by the monitor. This approach permits advances in specifying the low-level data collection, ...

17 The interactive performance of SLIM: a stateless, thin-client architecture
Brian K. Schmidt, Monica S. Lam, J. Duane Northcutt
December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth ACM symposium on Operating systems principles SOSP '99**, Volume 33 Issue 5
Publisher: ACM Press
Full text available: pdf(1.79 MB) Additional Information: full citation, abstract, references, citings, index terms

Taking the concept of thin clients to the limit, this paper proposes that desktop machines should just be simple, stateless I/O devices (display, keyboard, mouse, etc.) that access a shared pool of computational resources over a dedicated interconnection fabric --- much in the same way as a building's telephone services are accessed by a collection of handset devices. The stateless desktop design provides a useful mobility model in which users can transparently resume their work on any desktop c ...

18 XRAY: Instrumentation for multiple computers
Russ Blake
May 1980 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1980 international symposium on Computer performance modelling, measurement and evaluation PERFORMANCE '80**, Volume 9 Issue 2
Publisher: ACM Press
Full text available: pdf(905.42 KB) Additional Information: full citation, abstract, references, citings, index terms

XRAY presents a global view of the performance of hardware and software components on multiple, distributed computers. The set of components chosen for measurement can be changed at any time throughout a network of systems, and can be selected to minimize data collection time and measurement space. In the course of normal activities the operating system executes firmware which increments counters for the measured components. Periodically, the counters are

recorded in an ordinary file by a p ...

19 Experience with an adaptive globally-synchronizing clock algorithm
Cheng Liao, Margaret Martonosi, Douglas W. Clark
June 1999 **Proceedings of the eleventh annual ACM symposium on Parallel algorithms and architectures**
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20 Security: SECA: security-enhanced communication architecture
Joel Coburn, Srivaths Ravi, Anand Raghunathan, Srimat Chakradhar
September 2005 **Proceedings of the 2005 international conference on Compilers, architectures and synthesis for embedded systems CASES '05**
Publisher: ACM Press
Full text available: pdf(396.53 KB) Additional Information: full citation, abstract, references, index terms

In this work, we propose and investigate the idea of enhancing a System-on-Chip (SoC) communication architecture (the fabric that integrates system components and carries the communication traffic between them) to facilitate higher security. We observe that a wide range of common security attacks are manifested as abnormalities in the system-level communication traffic. Therefore, the communication architecture, with its global system-level visibility, can be used to detect them. The communicati ...

Keywords: AMBA Bus, access control, architecture, attacks, bus, communication, digital rights management (DRM), intrusion detection, security, security-aware design, small embedded systems, system-on-chip (SoC)

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K Cho, H Samueli, CN MicroSyst, CA Los Angeles - IEEE 2000 Custom Integrated Circuits Conference, 2000 - ieeexplore.ieee.org
... Seven MSB bits out of the 32-**bit** output are used ... a spectrum analyzer is used to **monitor** the spectra ... QAM Modulation Output Spectrums with Various IF-**frequency** ...
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Prototype Real-Time Baseband Signal Combiner
LD Howard, R **Frequency**, M SubsystemsSection - The Telecommunications and Data Acquisition Progress Report, ..., 1980 - tmo.jpl.nasa.gov
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An S-FSK Power Line Carrier Modem for Utility AMR applications
B Goffart, P Desneux, T Verbeure, P Gonthier, R ... - Metering and Tariffs for Energy Supply, 1999. Ninth ..., 1999 - ieeexplore.ieee.org
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... vibration data is measured over a **frequency** range of ... functions in a combination of **hardware** and **firmware** ... status from the running of the performance **monitor BIT**. ...
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A Practical ADSL Technology Following a Decade of Effort
T Network - IEEE Communications Magazine, 2001 - hit.bme.hu
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A DIGITAL HIGH-PERFORMANCE MULTI-STANDARD VIDEO DATA SLICER Jinan Lin and Maximilian Erbar Technical ...
I INTRODUCTION, IIS CONCEPT - IEEE Transactions on Consumer Electronics, 1998 - ieeexplore.ieee.org
... on a TV screen or a computer **monitor**. ... configurable conditions of timing, transmission **frequency**, amount of ... clock run-in for **bit**-synchronization, • framing ...
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A practical ADSL technology following a decade of effort

P Reusens, D Van Bruyssel, J Sevenhans, S Van Den ... - IEEE Communications Magazine, 2001 - ieeexplore.ieee.org
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J McKay, J Vrba, K Betts, MB Burbank, S Lee, K ... - Proceedings of 1993 Canadian Conference on Electrical and ..., 1993 - ieeexplore.ieee.org
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